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🔍 Title: **JP11119256A2: ACTIVE MATRIX LIQUID CRYSTAL DISPLAY DEVICE**

🔍 Derwent Title: Surge protective circuit in active matrix liquid crystal display device - impresses surge voltage to scanning line or signal line, based on which electric charge is generated by corresponding potential line [\[Derwent Record\]](#)

🔍 Country: JP Japan

🔍 Kind: A (See also: [JP3111944B2](#))

🔍 Inventor: MATSUMOTO SEIICHI;
OI SUSUMU;

🔍 Assignee: NEC CORP
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🔍 Published / Filed: 1999-04-30 / 1997-10-20

🔍 Application Number: JP1997000286892

🔍 IPC Code: G02F 1/136; G02F 1/133;

🔍 Priority Number: 1997-10-20 [JP19971997286892](#)
1997-10-20 [JP1997000286892](#)

🔍 Abstract: PROBLEM TO BE SOLVED: To reduce the power consumption at the time of driving an active matrix liquid crystal display device (LCD) by forming surge protection circuits for reducing defects caused by static electricity in the outer periphery of a thin film transistor (TFT) array and reducing the value of a current flowing between a scanning line and a reference potential line for discharging electric charge.

SOLUTION: A scanning line side reference potential line 31 arranged rectangularly to respective scanning lines, the surge protection circuits 28, 29 for connecting respective scanning lines 3 to the line 31, a signal line side reference potential line 32 arranged rectangularly to respective signal lines 4, and the surge protection circuits 26, 27 for connecting respective signal lines 4 to the line 32 are arranged in the outer periphery of an image area of the TFT array in the LCD, and when surge voltage is impressed to the scanning line 3 or the signal line 4, charge is discharged to the line 31 or 32.

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🔍 Other Abstract Info: None





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